## DECLARATION FOR PATENT APPLICATION

AUS 1 0 2004

As a delow named inventor, I declare: that I verily believe myself to be the original, first and sole (if only one delividual inventor is listed below) or an original, first and joint inventor (if more than one individual inventor is listed below) of the invention in

CONTROL CIRCUIT FOR ERROR CHECKING AND CORRECTION AND MEMORY CONTROLLER

the specification of which is attached hereto unless the following box is checked.

was filed on January 28, 2004 as United States Application or PCT International Application No. 10/765,066, and was amended on (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above. I acknowledge the duty to disclose information of which is material to patentability as defined in 37 CFR 1.56. I hereby claim foreign priority benefits under 35 U.S.C. 119(a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or 35 U.S.C. 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also

least one country other than the United States, listed below and have also identified below any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed:

CountryCategoryApplication No.Filing DatePriority ClaimJapanPatent2003-024861January 31, 2003Yes

And I hereby appoint Douglas B. Henderson (Reg. No. 20, 291). Ford F. Farabow, Jr. (Reg. No. 20, 630), Arthur S. Garrett (Reg. No. 20, 338), Donald R. Dunner (Reg. No. 19, 073), Brian G. Brunsvold (Reg. No. 22, 593), Tipton D. Jennings, IV (Reg. No. 20, 645), Jerry D. Voight (Reg. No. 23, 020), Laurence R. Hefter (Reg. No. 20, 827), Kenneth E. Payne (Reg. No. 23, 098), Herbert H. Mintz (Reg. No. 26, 691), C. Larry O'Rourke (Reg. No. 26, 014), Albert J. Santorellii (Reg. No. 22, 610), Michael C. Elmer (Reg. No. 25, 857), Richard H. Smith (Reg. No. 20, 609), Stephen L. Peterson (Reg. No. 26, 325), John M. Romary (Reg. No. 26, 331), Bruce C. Zotter (Reg. No. 27, 680), Dennis P. O'Reilley (Reg. No. 27, 932), Allen M. Sokal (Reg. No. 26, 695), Robert D. Bajefsky (Reg. No. 25, 387), Richard L. Stroup (Reg. No. 28, 478), David W. Hill (Reg. No. 28, 2820), Thomas L. Irving (Reg. No. 27, 665), Basil J. Lewris (Reg. No. 28, 165), Thomas W. Winland (Reg. No. 27, 665), Basil J. Lewris (Reg. No. 28, 188), Martin I. Fuchs (Reg. No. 28, 508), E. Robert Yoches (Reg. No. 30, 120), Barry W. Graham (Reg. No. 29, 924), Susan Haberman Griffen (Reg. No. 30, 907), Richard B. Racine (Reg. No. 30, 415), Thomas H. Jenkins (Reg. No. 30, 857), Robert E. Converse, Jr. (Reg. No. 27, 432), Clair X. Mullen, Jr. (Reg. No. 30, 907), Richard B. Racine (Reg. No. 31, 354), John C. Paul (Reg. No. 30, 413), David M. Kelly (Reg. No. 30, 953), Kenneth J. Meyers (Reg. No. 31, 934), Barbara C. McCurdy, (Reg. No. 32, 205), James K. Hammond (Reg. No. 31, 964), Richard V. Burgujian (Reg. No. 32, 120), James K. Hammond (Reg. No. 32, 984), Richard V. Burgujian (Reg. No. 32, 120), James K. Hammond (Reg. No. 31, 964), Richard V. Burgujian (Reg. No. 31, 744), J. Michael Jakes (Reg. No. 32, 824), Thomas W. Banks (Reg. No. 32, 13), M. Paul Barker (Reg. No. 32, 984), Richard V. Burgujian (Reg. No. 31, 744), J. Michael Jakes (Reg. No. 32, 824), Thomas W. Banks (Reg. No. 32, 13), M. Paul Barker (Reg. No. 32, 984), Robert E. Converse is a substitution and revocation,

the application or any patent issued thereon.



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I declare further that my mailing address is at c/o
Intellectual Property Division, Toshiba Corporation, 1-1, Shibaura
1-chome, Minato-ku, Tokyo 105-8001, Japan; and
that my citizenship and residence are as stated below next to my name:

Inventor: (Signature)	Date	Residence
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